



02981.000005.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	:	Examiner: NYA
FRANZ KUTTNER ET AL.	)	
	:	Group Art Unit: NYA
Application No.: 10/691,507	)	
	:	
Filed: October 24, 2003	)	
	:	
For: METHOD AND CIRCUIT	)	
CONFIGURATION FOR MIXING	:	
A DIGITAL SIGNAL WITH AN	)	
ANALOGUE SIGNAL	)	November 18, 2003

Mail Stop  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION OF PRIORITY DOCUMENT

Sir:

In support of Applicants' claim for priority under 35 U.S.C. § 119, enclosed  
is a certified copy of the following foreign application:

DE 102 50 632.9, filed October 30, 2002.

Applicants' undersigned attorney may be reached in our New York office by

telephone at (212) 218-2100. All correspondence should continue to be directed to our address given below.

Respectfully submitted,

  
\_\_\_\_\_  
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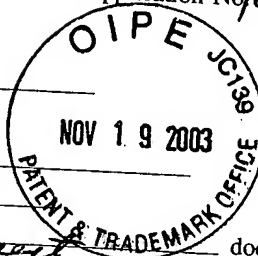
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Date 11/19/03  
Mo. Day Yr.  
Atty. Docket 02981-000005  
Application No. 10/691,507

Sir:

Kindly acknowledge receipt of the accompanying:

- ☐ Response to Official Action. \_\_\_\_\_  
☐ Check for \$ \_\_\_\_\_ (claims fee)  
☐ Petition under 37 CFR 1.136 and Check for \$ \_\_\_\_\_  
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☐ Information Disclosure Statement, PTO-1449 and \_\_\_\_\_ documents  
☒ *Submission of Priority Document* Claim for priority and certified copy of \_\_\_\_\_ priority applications  
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37 CFR 1.8 ☐  
37 CFR 1.10 ☐  
By Hand ☒

FCIS-B-00



**Prioritätsbescheinigung über die Einreichung  
einer Patentanmeldung**

**Aktenzeichen:** 102 50 632.9  
**Anmeldetag:** 30. Oktober 2002  
**Anmelder/Inhaber:** Infineon Technologies AG,  
München/DE  
**Bezeichnung:** Verfahren und Schaltungsanordnung zum Mischen  
eines Digitalsignals mit einem Analogsignal  
**IPC:** H 03 D 7/00

**Die angehefteten Stücke sind eine richtige und genaue Wiedergabe der ur-  
sprünglichen Unterlagen dieser Patentanmeldung.**

München, den 23. Oktober 2003  
**Deutsches Patent- und Markenamt**  
**Der Präsident**  
Im Auftrag

  
Wernitz

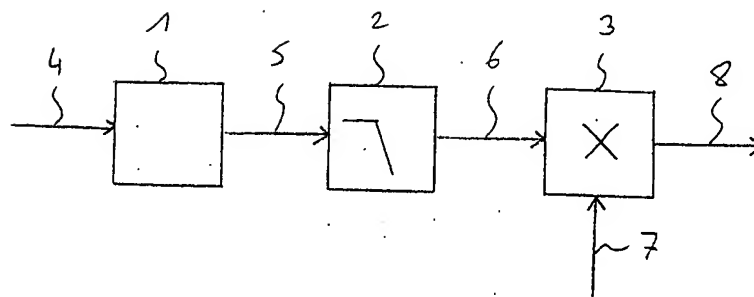


Fig. 1

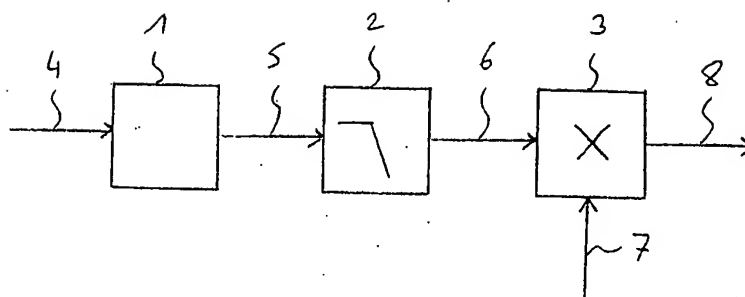


Fig. 1

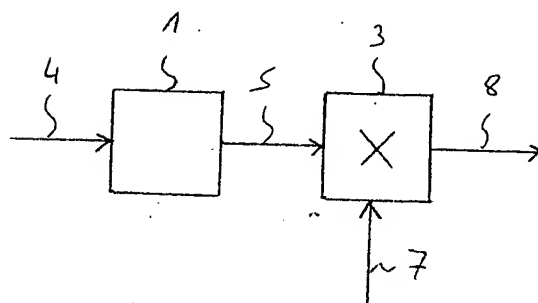


Fig. 2

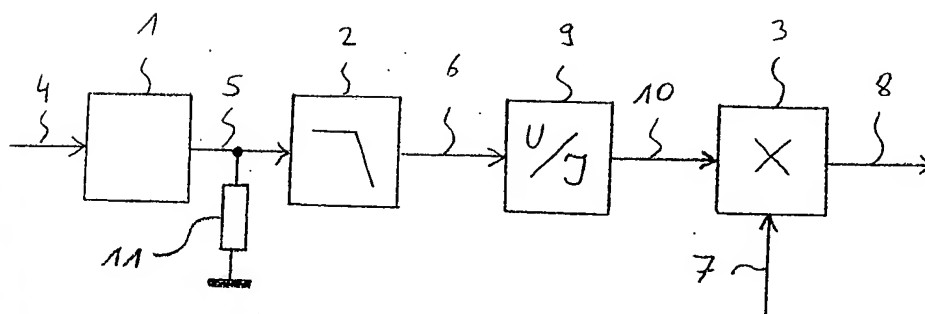


Fig. 3